

P-CHANNEL MOS FIELD EFFECT TRANSISTOR
 FOR SWITCHING

DESCRIPTION

The μ PA1912 is a switching device which can be driven directly by a 2.5-V power source.

The μ PA1912 features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

FEATURES

- Can be driven by a 2.5-V power source
- Low on-state resistance
 $R_{DS(on)1} = 50 \text{ m}\Omega \text{ MAX. (} V_{GS} = -4.5 \text{ V, } I_D = -2.5 \text{ A)}$
 $R_{DS(on)2} = 52 \text{ m}\Omega \text{ MAX. (} V_{GS} = -4.0 \text{ V, } I_D = -2.5 \text{ A)}$
 $R_{DS(on)3} = 70 \text{ m}\Omega \text{ MAX. (} V_{GS} = -2.5 \text{ V, } I_D = -2.5 \text{ A)}$

ORDERING INFORMATION

PART NUMBER	PACKAGE
μ PA1912TE	6-pin Mini Mold (Thin Type)

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C)

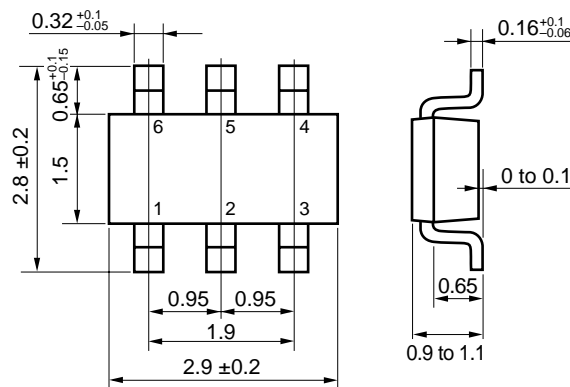
Drain to Source Voltage	V _{DSS}	-12	V
Gate to Source Voltage	V _{GSS}	±10	V
Drain Current (DC)	I _{D(DC)}	±4.5	A
Drain Current (pulse) ^{Note1}	I _{D(pulse)}	±18	A
Total Power Dissipation	P _{T1}	0.2	W
Total Power Dissipation ^{Note2}	P _{T2}	2	W
Channel Temperature	T _{ch}	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

- Notes 1.** PW ≤ 10 μs, Duty Cycle ≤ 1 %
2. Mounted on FR-4 board, t ≤ 5 sec.

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

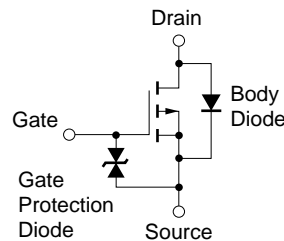
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 Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

PACKAGE DRAWING (Unit : mm)



- 1, 2, 5, 6 : Drain
 3 : Gate
 4 : Source

EQUIVALENT CIRCUIT

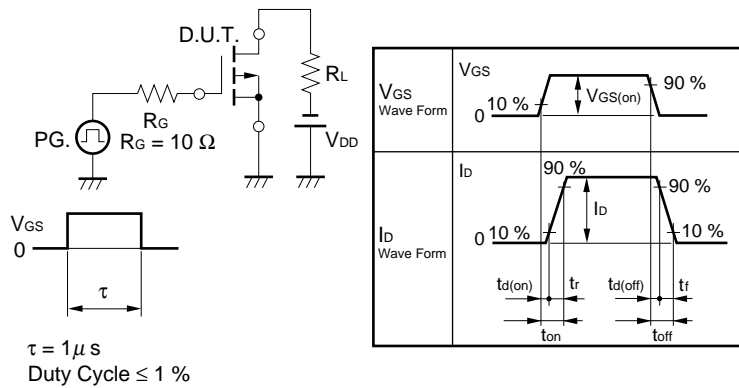


Marking: TD

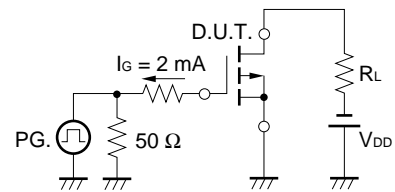
ELECTRICAL CHARACTERISTICS (T_A = 25 °C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -12 V, V _{GS} = 0 V			-10	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±10 V, V _{DS} = 0 V			±10	μA
Gate to Source Cut-off Voltage	V _{GS(off)}	V _{DS} = -10 V, I _D = -1 mA	-0.5	-0.90	-1.5	V
Forward Transfer Admittance	y _{fs}	V _{DS} = -10 V, I _D = -2.5 A	3	9.3		S
Drain to Source On-state Resistance	R _{DS(on)1}	V _{GS} = -4.5 V, I _D = -2.5 A		39	50	mΩ
	R _{DS(on)2}	V _{GS} = -4.0 V, I _D = -2.5 A		40	52	mΩ
	R _{DS(on)3}	V _{GS} = -2.5 V, I _D = -2.5 A		53	70	mΩ
Input Capacitance	C _{iSS}	V _{DS} = -10 V		810		pF
Output Capacitance	C _{oSS}	V _{GS} = 0 V		241		pF
Reverse Transfer Capacitance	C _{rSS}	f = 1 MHz		122		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = -6 V		304		ns
Rise Time	t _r	I _D = -2.5 A		532		ns
Turn-off Delay Time	t _{d(off)}	V _{GS(on)} = -4.0 V		406		ns
Fall Time	t _f	R _G = 10 Ω		796		ns
Total Gate Charge	Q _G	V _{DD} = -10 V		5.6		nC
Gate to Source Charge	Q _{GS}	I _D = -4.5 A		2.2		nC
Gate to Drain Charge	Q _{GD}	V _{GS} = -4.0 V		2.6		nC
Diode Forward Voltage	V _{F(S-D)}	I _F = 4.5 A, V _{GS} = 0 V		0.86		V
Reverse Recovery Time	t _{rr}	I _F = 4.5 A, V _{GS} = 0 V		1.1		μs
Reverse Recovery Charge	Q _{rr}	di/dt = 10 A/μs		4.3		μC

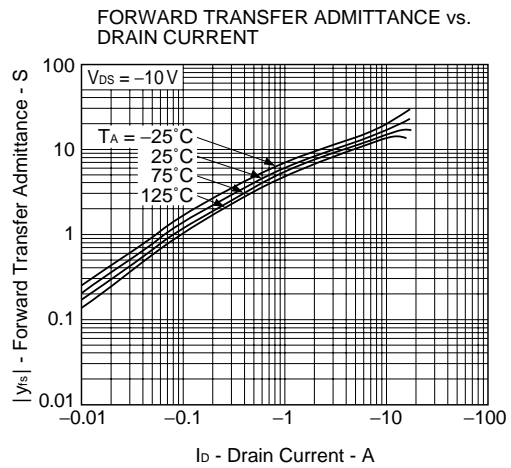
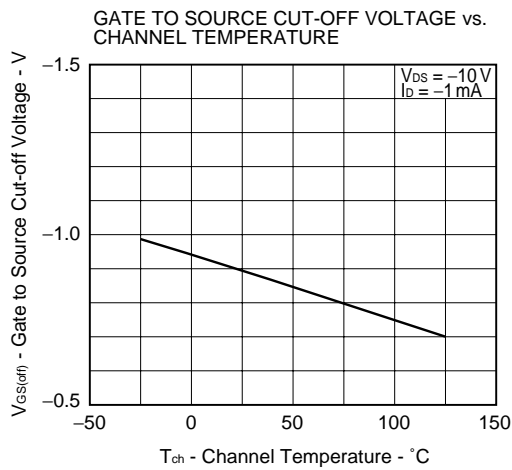
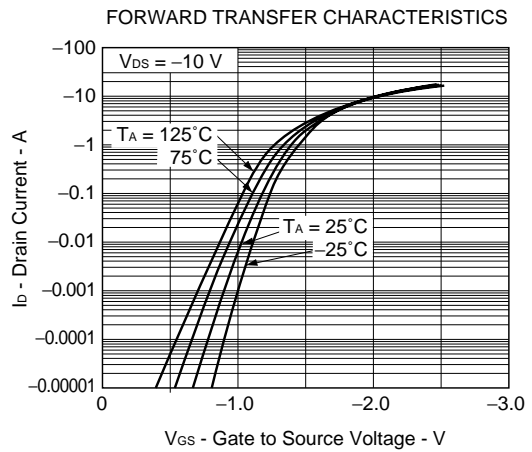
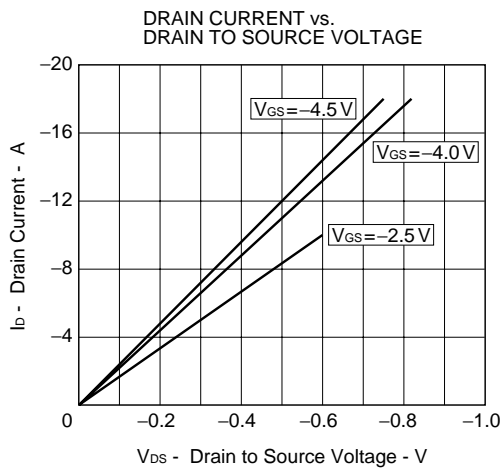
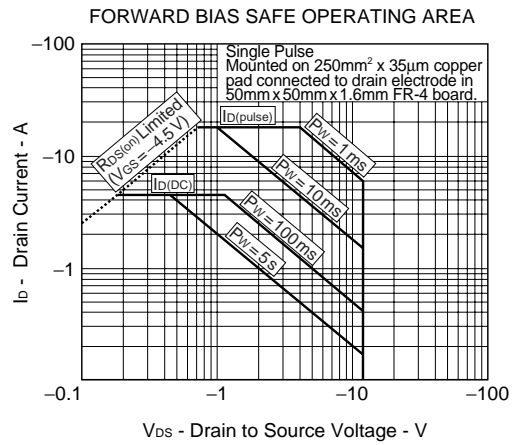
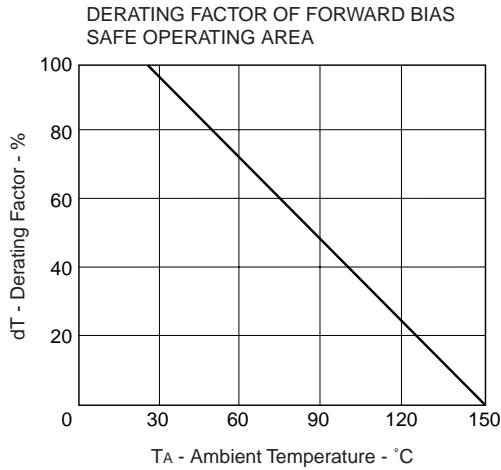
TEST CIRCUIT 1 SWITCHING TIME

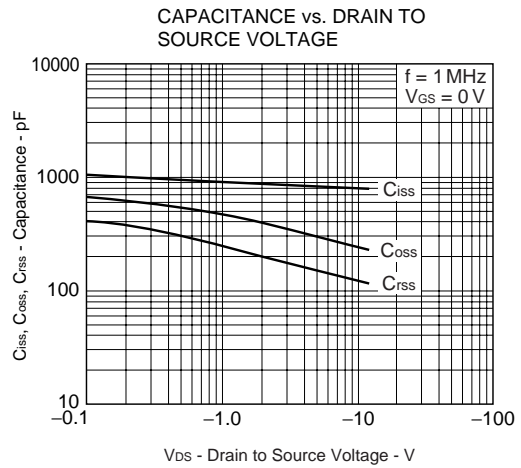
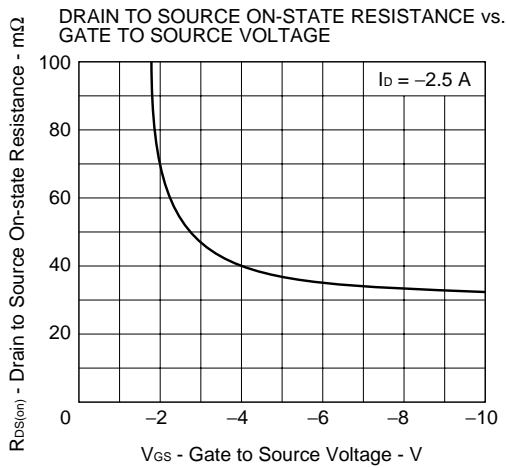
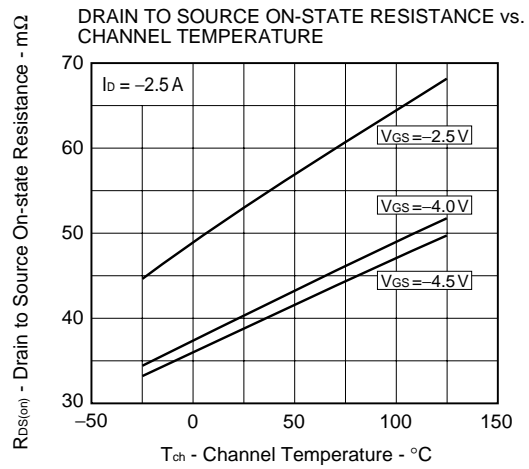
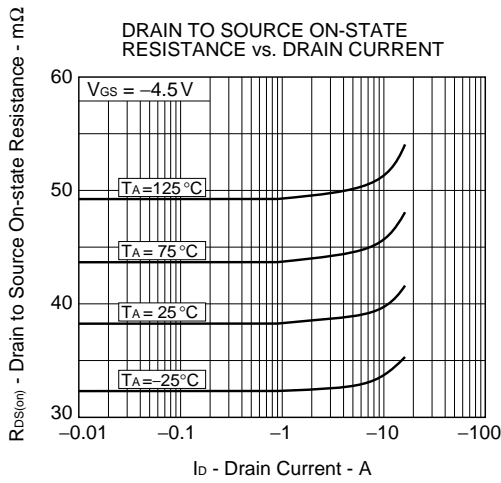
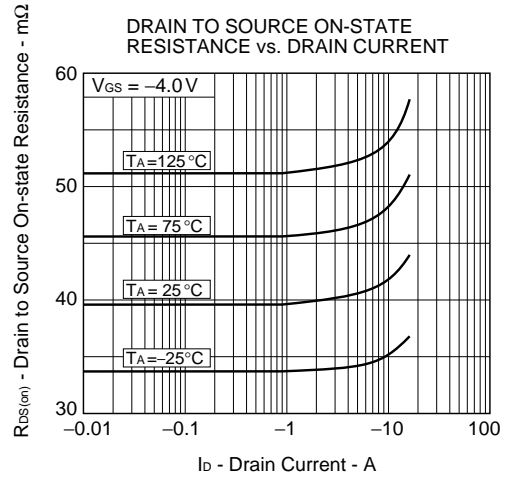
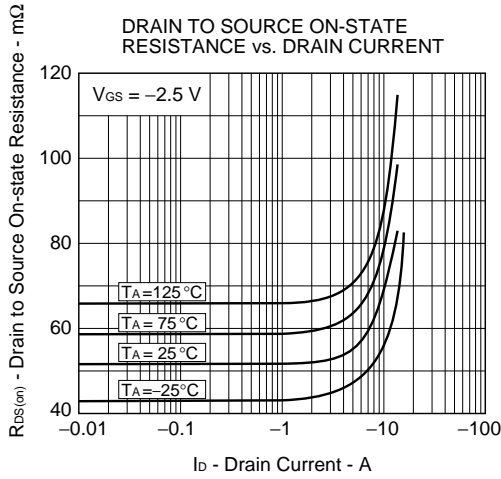


TEST CIRCUIT 2 GATE CHARGE

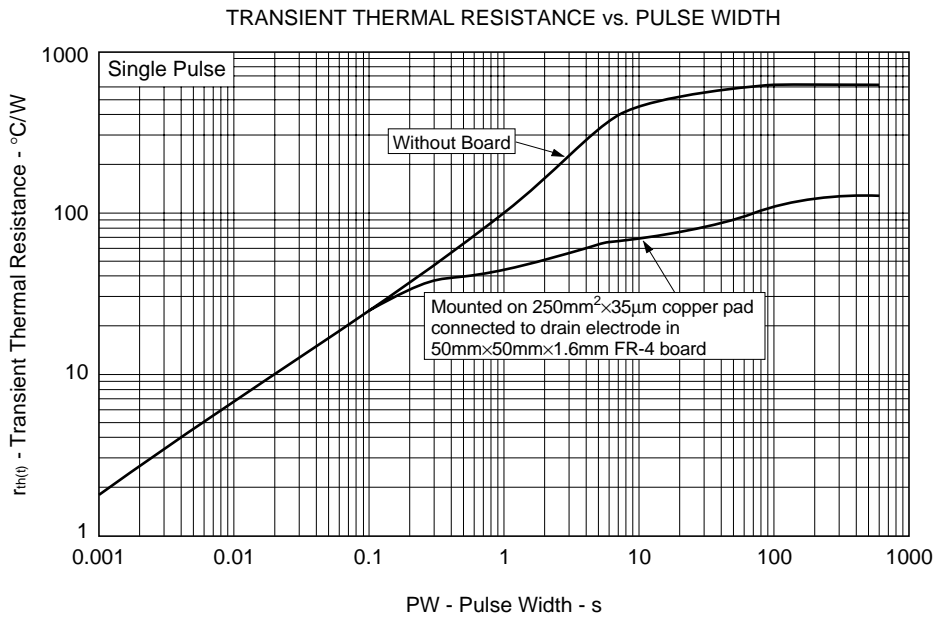
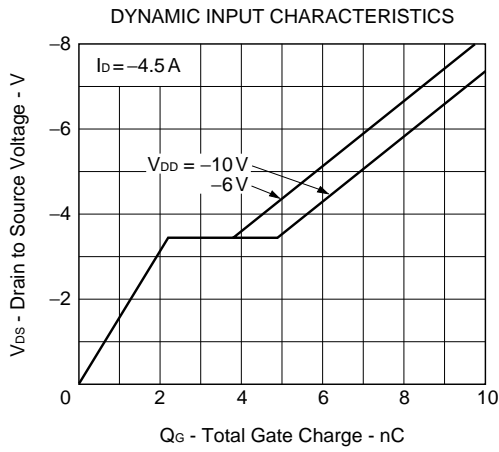
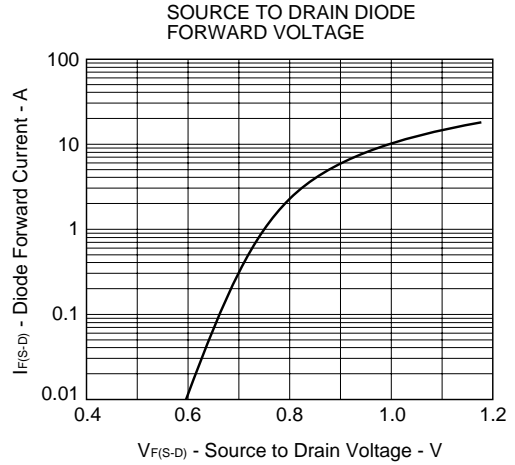
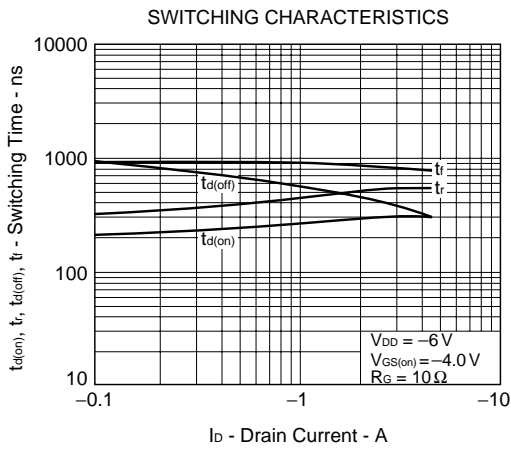


TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$)





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